IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hugh Barrass et al.

Serial No.:

10/035,353

Filing Date:

December 28, 2001

Group Art Unit:

2663

Title:

Method and System for Distributing Data Communications

Utilizing a Crossbar Switch

Mail Stop: Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

DECLARATION UNDER 37 C.F.R. § 1.131

- I, Chad C. Walters, attorney for Applicants for the above-identified Patent Application, hereby make the following declaration:
- 1. I am an attorney for Applicants for the above-referenced Application entitled "Method and System for Distributing Data Communications Utilizing a Crossbar Switch," filed on December 28, 2001.

- 2. The Examiner rejects Claims 1-3, 5, 7, 10-14, 18, 20-23, 30 and 36-45 of the current Application in an Official Action mailed February 7, 2006 based, in whole or in part, on U.S. Patent No. 6,990,095, entitled "Self-Routing Data Switching System," filed on October 2, 2001 (the "Effective Date") and issued to Wu et. al. on January 24, 2006 (the "Wu Patent").
- 3. Prior to the Effective Date, I received documents from the inventors detailing the subject matter of the above-referenced invention. Some of the inventor disclosure is attached as Exhibits A, B and C. These documents are dated prior to the Effective Date and disclose at least all of the concepts embodied within the rejected claims (Claims 1-3, 5, 7, 10-14, 18, 20-23, 30 and 36-45) of the present invention. The dates and other selected confidential information of the client have been redacted.
- 4. Prior to the Effective Date and up to our filing date of December 28, 2001, I worked with the inventors to develop an application embodying the concepts disclosed to me. The attached Exhibit D details billing records of attorneys for Applicants in the months leading up to the filing of the present application on December 28, 2001, with the dates and other selected confidential information of the client redacted. These billing records demonstrate some of our continuing activities during the time period between the Effective Date and the filing of the Application at issue.
- 5. I declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine, imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the Application or any patent issuing thereon.

Executed this 8th day of May, 2006.

Chad C. Walters

Exhibit A

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ildea No.	Title		linventors	Entered	Updated	Desig Rev
125518	Low cost crossbar for Long Reach Ethernet (VDSL) technology	Hugh Barr Schwartz (rass (hbarrass),David (davids)			

CISCO CONFIDENTIAL

125518: Low cost crossbar for Long Reach Ethernet (VDSL) <u>vehinology</u>

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Full Details

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eiomevni (AS Submitted)

Hugh Barrass (hbarrass)

Work Phone: 408 527-3084

Manager: davids

Department: EAG BBSU Engineering Division: EAG

Site:

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SAN JOSE

Background

It is required to provide Long Reach Ethernet (VDSL-based) service to an arbitrary (but small) number of users in an installation consisting of many potential users.

In order to provide a cost-effective solution, a mechanism is needed which allows a small number of LRE switch ports to provide service to a large number of potential users.

Summary

This invention consists of a partially populated crossbar (which does not provide connectivity from any port to any port) and some extra ports which "sweep" all of the uncommitted lines to search for new users.

Advantages

The partially populated crossbar allows costs and technical difficulties to be minimized.

The "sweeper" ports allow user self-provisioning without human intervention.

Exhibit B

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Italia No.	Title	in linven	tors .	#Entered	Updated	Desig
131048	Solid state crossbar for VDSL port provisioning	Hugh Barrass (hba Schwartz (davids)	rrass),David	7,50		

CISCO CONFIDENTIAL

48. Solid state crossbar for VDSL pont provisioning

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Background

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In order to provide a cost-effective solution, a mechanism is needed which allows a small number of LRE switch ports to provide service to a large number of potential users.

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This invention consists of a crossbar switch implemented using silcon transistor technology to provide the variable connection between the switch (or DSLAM) and the lines connecting to the users.

Current solutions include relay based crossbar switches - these have slower switching times and much shorter working lives (limiting the average switching frequency to ~1 per day instead of ~1 per millisecond).

Silicon transistor based crossbar solutions have been used in many other applications including: network packet switching; super-computer backplanes; video mutlipexing; voice and low-speed data switching. This is the first known application of silicon transistor based crossbars to Very High Bit Rate DSL technology.

The speed and frequency of switching enable many novel solutions which would be impossible with relay-based switches.

The use of VDSL enables higher data rates which could support new applications unavailable using prior technologies.

Exhibit C

2.2 Connection table (1-16 switches)

This configuration yields at least 3 overlapping ports which allows clusters of up to 5 adjacent live users.

Examining the table shows that clusters of 5 live users may be supported in almost all cases. Only a few gaps exist which will only allow clusters of 4 live users. This is much more likely to be acceptable.

Switch port 2 2 2 1 1 1 2 2 1 Line 1 2 3 5 2 3 5 3 1 x x 2 3 x x x x 5 x x 6 7 x x x x x x x 10 x x x x 11 x x x 12 x x x x x x 13 x x x x x 14 15 x x x x 16 x x x x x x 17 x x x 18

Table 2: Connection Table - 1-8

Table 2: Connection Table - 1-8

Switch port										1	1	1 :	1	1	1	1	1	1	1	1	1	2	2	2	2	2
Line	1	2	3	4	5	6	7	8	9	0	1	1 2	2	3	4	5	6	7	8	9	(,	1	2	3	4
19			x	x	х	х	х					T			7						T	\top	1			
20			Γ	x	х	х	x					\top	T									1	1			
21				x	x	x	x					1		\top	7						\top	T	+			
22				х	x	x	x				Τ	T		1	1						T	\top	\top			
23					x	х	х	x	Γ						1						T	\dagger	\top			
24					x	x	x	x				1	1	1	\top					\vdash	T	1	十	\exists		
25					х	x	х	x				Τ	\top		1							1	\dagger			
26					х	х	х	х					\top	1	1							T	1		\exists	
27						х	x	x	х				T		\top							1	T			
28						x	х	х	х						\exists					-		T	+	一	7	_
29						x	х	х	х						1	7						1	1	1	\exists	
30						x	х	х	х				\top	1	1	\exists							\top	1	7	
31							х	x	х	х		Γ	T		\top							T	T	1	\top	
32							x	x	х	x	Г		T		1		T						1	7		
33							х	х	x	x					Τ	7							\top	\top		
34							x	x	x	x			Π	T	1								T		7	
35								x	х	x	x				7									1	1	
36								х	х	х	х				T		7						Τ		1	_
37								x	х	х	x					T							T			_
38								x	х	x	x				T									1		_
39									x	х	x	х			T								T	T	T	
40									x	x	x	x			T			7					Γ	7		
41									x	x	х	x			Τ	1	7	\top					\top	\top		
42									x	x	x	x			Τ	Τ	T	7					Γ	\top	1	_
43										x	х	x	x		T				T					1		_
44							\neg			x	x	x	x		Τ	1										_
45										x	x	x	х			\top	1	7		\top				\top	1	_
46				7				\exists		x	x	x	х		Γ	T	1	1	\top	7				\top	1	_
47	1	\exists	\exists						\exists		x	х	x	x		\top	1	1	\top	\top			Г	T	1	-
48						\exists					x	x	x	х	Γ	1	7	\top	+	一	7			T	\top	_
49			\top		\exists	7	\exists					x	x	x	1	1	十	1	+	7		-	Г	T	+	
50	1	\top	7		+	1	\top	7		\dashv	7	x	x	x	Γ	1	+	+	+	1	7		-	T	1-	_
51	\top	\top	+	+	\top	\top	+	7	7	1	\dashv	x	x	x	x	1	T	1	-	+	\dashv		-	\dagger	+	-
52	\top	\top	\top	\dagger	\dagger	+	\dashv	+	\dashv	\top	\dashv	×	x	x	x	+-	十	+	\top	+	\dashv			\vdash	+	-
			!_					Щ.		i_	L	1			Щ.							i		<u>L</u>		_

Table 2: Connection Table - 1-8

Switch port										1	1	1	1	1	1	1	1	1 1	ı	1	2	2	2	2	2
Line	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6	7	/ 8	3	9	0	1	2	3	4
53												х	х	х	х		\vdash	+	\dagger	\forall					
54								-			Г	x	х	x	х			\dagger	1	7	\dashv	\dashv			
55													х	x	х	x		T	1	\top	7	\dashv			
56									_				х	х	х	х		\top	†	\dagger	7				
57													х	х	x	x			†	\dagger	7				
58													х	х	х	x		T	1	\dagger	\top	7	\neg		
59														х	х	x	x		T	+	1	7		7	
60													\exists	x	х	х	х			†	+	\forall			
61														x	х	x	x	Γ	T	1	1	\top	\exists		
62														x	х	х	х	T	<u> </u>	T	†	7		\exists	
63			• •												x	х	x	x	T	\dagger	1	\top	\exists	7	
64															х	x	х	x		1	\top	1	7	\exists	
65															х	x	x	x		T	1	7		1	
66															x	x	х	x	Γ	1	\top	1	1		
67													٦			ж.	x	x	x	T	1	1	\top	\top	
68					П					\neg			7			x	х	х	x		\top		1	7	_
69										\neg			\exists		\top	х	х	x	x		1	\top	1	1	
70																x	х	х	x				T	1	
71																	x	x	x	x	T	T		\top	_
72																	x	х	x	x	Τ	T			
73																	x	x	x	x		T			
74													Ţ				x	x	х ·	x					
75																7		x	x	x	×	T	\top		
76																		x	х	x	x	T			
77										\neg								x	х	x	x	1	1	1	_
78										$\neg \vdash$	T				7			x	х	х	×	x		1	_
79 .									Τ.							7			х	х	x	x	1	\top	
80																1			х	x	x	x	x		_
81								\top											x	x	x	x	×	x	_
82															\top			\exists	x	x	x	x	×	×	
83						\top			1		1		1	1	\top	1	+	1	\dashv	x	x	×	×	×	_
84	7	1	7	\top	\top	T	1	1	7	1	1	\top	1	1	1	+	+	\top	\dashv	x	х	x	╂	+-	-
85	1	\top	\top	1	1	1		1	+	+	\top	1	+	†	+	+	1	\top	7	x	x	x	×	╁	_
86	†	\top	1	1	1	\dagger	T	1	1	1	+	1	\dagger	1	\top	\dagger	\dagger	+	\dashv	x	x	×	x		_

Table 2: Connection Table - 1-8

Switch port										1	1	1	1	1	1	1	1	1	1	2	2	2	2	2
Line	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6	7	8	9	0	1	2	3	4
87																					х	х	х	x
88																					х	x	х	x
89																					х	x	х	x
90																					х	х	х	x
91																						x	x	x
92																						х	x	x
93																						x	х	x
94																							x	x
95						\exists			\exists					\dashv	\dashv		\neg	\exists	\neg		1		x	x
96									コ	\neg			\neg	\neg	\dashv			\dashv		\neg	7	\dashv	_	x

2.3 Connection table (1-16 switches with sweeper)

This configuration is very similar to the 1-16 version but ports 23 and 24 cover all 96 lines.

The table shows that this configuration has the same overall behaviour to the previous one with two notable exceptions. Firstly the "sweeper" ports can be applied anywhere to cope with one or two large clusters. The second is that one of the sweepers can be used to monitor "potentially" live lines to allow genuine self-provisioning.

Table 3: Connection Table - 1-8

Switch port										1	1	1	1	1	1	1	1	1	1	2	2	2	2	
Line	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6	7	8	9	0	1	2	3	,
1	x																						s	s
2	x	x																					s	s
3	х	x																					s	s
4	x	x	x																				s	s
5	x	х	x																				s	s
6	x	x	x								•										\neg		s	s
7	x	x	x	х																	\neg		s	5
8	x	х	х	x													\exists		\neg			\neg	5	s
9	x	х	х	х											\neg		\exists			\neg		7	s	s
10	x	x	x	x					\neg				\neg							寸			s	5
11	x	х	х	х	x			\neg							\neg	\neg		7		7	\neg	7	s	s
12	×	×	x	x	x	\exists			\exists	\neg	ᅱ	\exists		\neg	\neg	\neg	\dashv	7	\dashv	7	\dashv	\dashv	s	s

Table 3: Connection Table - 1-8

Switch port										1		1	1	1	1	1	1	1	1	1	2	2	2	2	2
Line	1	2	3	4	5	6	7	8	9	0	,	1	2	3	4	5	6	7	8	9	0	1	2	3	4
13	х	х	х	x	х				T		1													s	s
14	х	х	х	х	х																			s	s
15	х	x	х	x	x	х												-						s	5
16	х	х	х	х	x	x				1		Ţ						-						s	s
17		х	x	х	х	х				Τ	7													s	s
. 18			x	х	х	х																		s	s
19			x	x	x	х	x			Π	T													s	s
20				x	x	x	x																	s	s
21				x	x	х	х					T												s	s
22				x	х	x	x						\top											s	s
23					x	x	x	x].	T												s	s
. 24					x	x	. x	x							•									s	s
25					x	х	x	x																s	s
26					x	x	х	x														7		s	s
27						x	х	x																s	s
28						x	x	x	x															s	s
29						x	x	x	x															s	s
30						х	x	x	x															s	s
31							х	x	x				\perp		$\cdot \rfloor$									s	s
32							x	x	x			L												s	s
33							x	x	x	x														s	8
34							x	x	x	x		L												s	s
35						\perp		×	x	x														s	s
36								x	x	x			\perp											s	s
37								x	x	x	x		\perp		\perp									s	<u> </u>
38								x	x	х	x												T	s	s
39									x	х	x													s	s
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42									x	x	х	x							1	7		\top		s :	 S
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45		\neg							\neg	x	x	x	Τ	T	\top	1	\top	7	1	\top			1	; s	_
46		1				7		1	\dashv	x	x	x	Τ	1	+	7	1	1	\top	+	1	\top	5	s	_

Table 3: Connection Table - 1-8

Switch port										1	1	1.	1	1	1	1	1	1	1	2	2	2	2	2
Line	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6	7	8	9	0	1	2	3	4
47										х	х	х	х										s	s
48										х	х	х	x										s	s
49											х	х	х							7			s	s
50											x	х	х										s	s
51											x	х	x										s	s
52											х	х	х	х									s	s
53											x	x	x	x									s	s
54												x	х	х									s	s
55												x	х	х									s	s
56												х	х	х	х								s	s
57												х	х	x	x					·			s	s
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60													х	х	х								s	s
61													x	х	х	x							s	s
62										-			x	x	х	x				•			s	s
63														x	х	x							s	s
64														х	x	х							s	s
65														х	x	x							s	s
66											-			x	x	x	x						s	s
67	ŀ													х	x	х	x						s	s
68															x	x	х						S	s
69															x	х	x						s	s
70															x	x	x						s	s
71															x	x	x	х					s	s
72																x	x	x					s	s
73																x	x	x					s	s
74																x	х	x					s	s
75			\Box		T											х	x	х	x				5	s
76						T										x	x	x	x				s	s
77				\exists				\top		\exists		\top	\top		7	\neg	x	х	x		\top		s	s
78	\top	+		\exists	7	\top		7	7	7		7	1	1	7		x	x	x	x		\top	s	<u>s</u>
79	7			_	7		7	7	\top	1	\top	+	1	1	1	7	x	x	x	x		\top	s ·	s
80	\dagger	+	十	+	\dashv	十	\top	\top	+	\top	十	\dashv	\top	7	\top	\dashv	x	x	x	x	x	+	s	 s

Table 3: Connection Table - 1-8

Switch port										1	1	1	1	1	1	1	1	1	1	2	2	2	2	2
Line	1	2	3	4	5	6	7	8	9	0	1	2	3	4	5	6	7	8	9	0	1	2	3	4
81																	х	х	х	х	x	х	s	s
82																		x	х	х	х	x	s	s
83																		х	х	х	x	х	s	s
84																		х	x	х	х	х	s	s
85																		х	x	x	x	х	s	s
86	Ÿ																	x	х	х	х	х	s	s
87																			х	х	x	х	s	s
88				·															х	х	x	х	s	s
89																			x	x	х	x	s	s
90																			x	x	x	x	s	s
91																				x	x	х	s	s
92																				x	x	x	s	s
93																				х	х	x	s	s
94																					х	х	s	s
95																					x	x	s	s
96																					\sqcap	x	s	s

Exhibit D

BAKER BOTTS L.L.P. PRO FORMA STATEMENT

Client 062891

CISCO SYSTEMS, INC.

Matter 062891.0637

(4680) Solid State Crossbar for VDSL Port Provisioning Patent Application

DATE	INDEX	TK NO.	DESCRIPTION	WORKED HOURS	BILLABLE HOURS	BILLABLE	STAT	ACT CODE	
		••	Reviewing invention disclosure; conducting inventor interview with Mr. Barraes.	2.40					
			Reviewing technology and inventors' disclosure; preparing for interview; interviewing inventor.	4.00					
			Reviewing interview notes; formulating strategy for claims, drafting claims.	5.30					
			Formulating strategy for claims and figures; drafting claims, figures and detailed description.	7.50					
			Drafting detailed description, claims and figures.	3.40					
			Drafting claims, figures and detailed description.	8.40					
			Drafting detailed description, summary and background.	7.90					
			Reviewing and revising detailed description and background.	3.80					

BAKER BOTTS L.L.P. PRO FORMA STATEMENT

Client 062891 Matter 062891.0637 CISCO SYSTEMS, INC.

(4680) Solid State Crossbar for VDSL Port

DATE	INDEX	TK NO.	DESCRIPTION	WORKED HOURS	BILLABLE HOURS	BILLABLE AMOUNT	STAT	ACT CODE
			Final review and editing of application.	3.80				
			Drafting additional detailed description and claims; reviewing and revising detailed description, claims and figures.	7.00				
			Revising application based upon inventor's comments; preparing application for filing.	2.40				
Total Unbil	led Time Value	e		55.90				

Redacted -

Total Unbilled Costs